

CONTROL WIRING

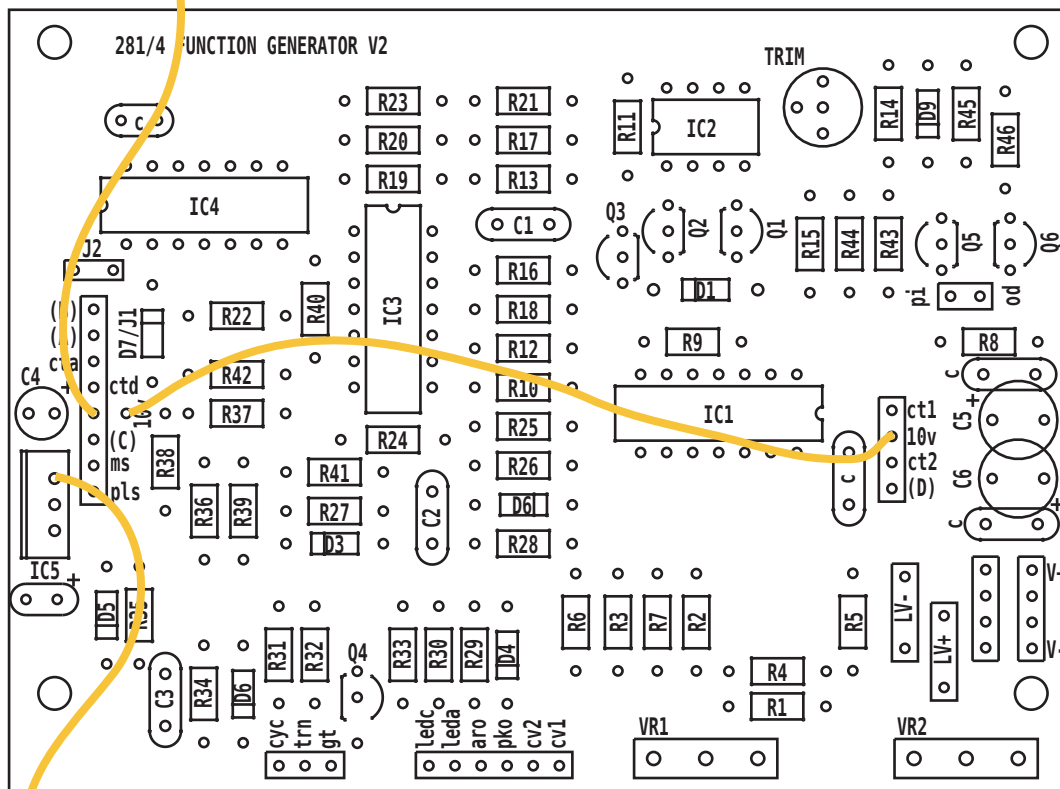
"cta" is "attack"
 "ctd" is "decay" □

"ct1" is linked to "VR1" and "cv1", and vice-versa, so the way this is wired here, "VR1" and "cv1" control the attack, or rising slope. you can wire "cta" to "ct2" to reverse the roles of the controls.

TO NEXT
 FG

ATT

DEC



10V POWER WIRING

10V+ has to be wired to IC1 and IC4.

the 10V regulator should be installed on the board where power enters from PSU. a single T0-220 regulator should be able to supply at least 4 FGs w/ out any problems. adequate filtering here is important.

bypass capacitors should be evenly distributed throughout the system. 10uF for each C5, C6 □ and at least 47uF for the 10V supply each 2 FGs. .1uf ceramic capacitors should be installed □ throughout, wherever space □ is given. □