

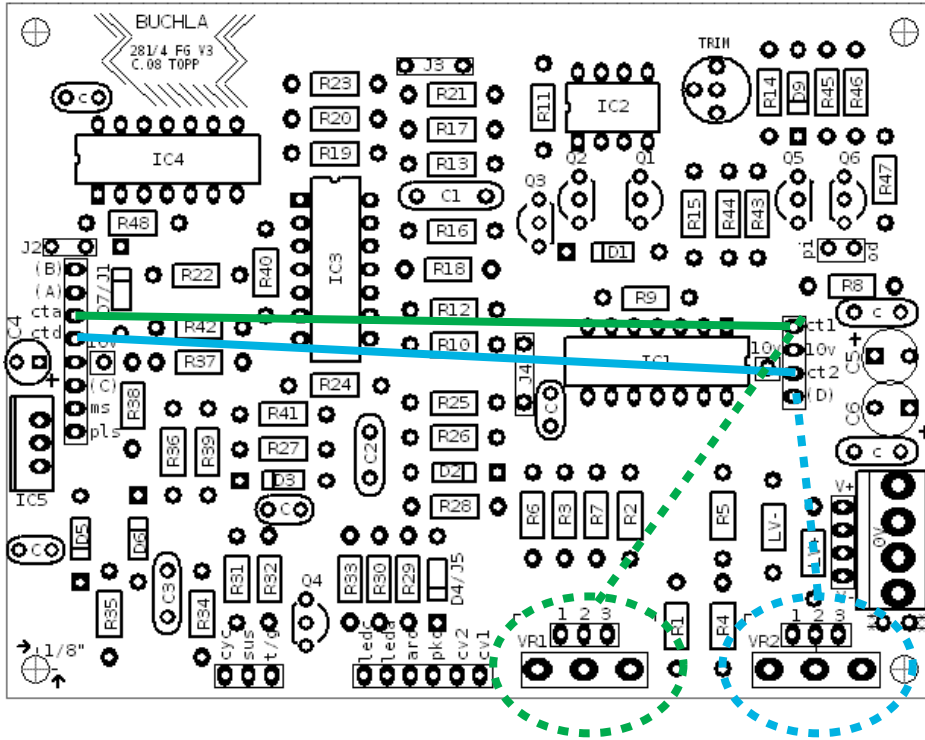
CONTROL & PWR WIRING V3

CONTROL WIRING

"cta" is "attack"
 "ctd" is "decay"

"ct1" is linked to "VR1" and "cv1", and vice-versa, so the way this is wired here, "VR1" and "cv1" control the attack, or rising slope. you can wire "cta" to "ct2" to reverse the roles of the controls.

NOTE that there are wire pads above the control pots if you don't want to PCB mount the pots



10V POWER WIRING

the 10V regulator should be installed on the board where power enters from the PSU. a single T0-220 regulator should be able to supply at least 4 FGs w/out any problems.

shown here are the 2 jumper locations (J3, J4) that run 10V from one side of the PCB to the other, also, circled are 10V pads that can be used to run 10V to other boards in the system.

bypass capacitors should be evenly distributed throughout the system. 10uF for each C5, C6 and at least 47uF for C4 per each 2 FGs. .1uf ceramic capacitors should be installed throughout, wherever space is given.

